

DALY, CROWLEY, MOFFORD & DURKEE LLP

354A Turnpike Street - Suite 301A
CANTON, MASSACHUSETTS 02021-2714

TELEPHONE: 781-401-9988

FACSIMILE: 781-401-9966

CHRISTOPHER S. DALY
JUDITH C. CROWLEY
DONALD F. MOFFORD
PAUL D. DURKEE

KERMIT ROBINSON
ANTHONY T. MOOSEY

OF COUNSEL
MARJANNE M. DOWNING

December 18, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RE: U.S. Patent of William E. Hoke et al.
Patent No.: 6,797,994
Issued on: September 28, 2004
Entitled: DOUBLE RECESSED TRANSISTOR
U.S. Appl. No.: 09/504,660
Our Ref. No.: RTN2-047PUS

Dear Sirs:

Enclosed is a completed copy of Form PTO/SB/44. It is requested that a Certificate of Correction be issued to correct the listed mistakes in the above-identified patent.

Authorization to charge the payment of \$100.00 to cover the cost of the Certificate of Correction in accordance with 37 C.F.R. §1.20(a) is hereby given. Please charge Deposit Account 50-0845.

Very truly yours,



Donald F. Mofford

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,797,994
DATED : September 28, 2004
INVENTOR(S) : Hoke et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2, lines 12-13 delete "the if Schottky layer" and replace with --the Schottky layer--.

Column 2, line 67 delete "test" and replace with --tests--.

Column 4, line 51 delete "layer 26 grown by MBE." and replace with --layer 26 is grown by MBE--.

Column 6, line 24 delete "30-50 E thick," and replace with --30-50 Å thick,--.

Column 7, line 26 delete "larger amount" and replace with --larger amounts--.

Column 7, line 40 delete "layer 120, 118" and replace with --layers 120, 118--.

Column 8, lines 29-30 delete "0.8 im and 1.2 im," and replace with --0.8 µm and 1.2 µm,--.

Column 8, line 63 delete "intermediate the" and replace with --intermediate to the--.

Column 10, line 17 delete "layer" and replace with --layer;--.

MAILING ADDRESS OF SENDER:

DALY, CROWLEY, MOFFORD & DURKEE, LLP
354A Turnpike Street, Suite 301A
Canton, MA 02021-2714

PATENT NO. 6,797,994

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